

Fig. 1

Prior Art

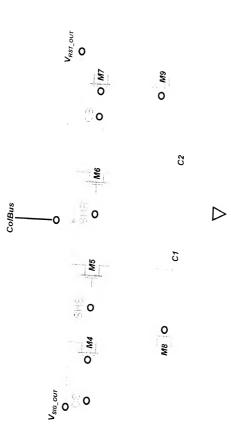
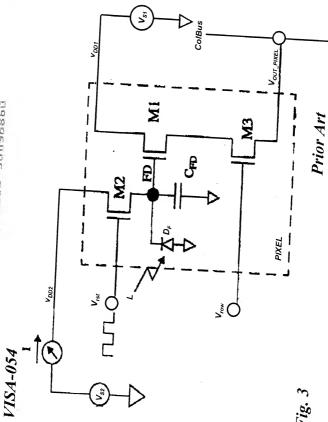


Fig. 2

Prior Art



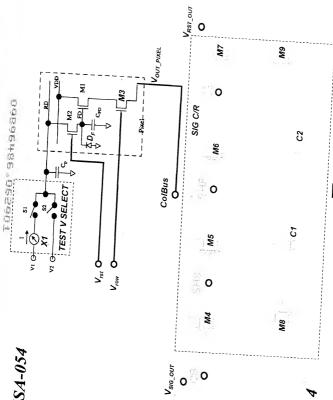


Fig. 4

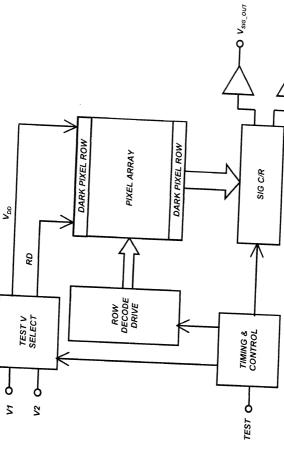


Fig 5

→ V_{RST_OUT}

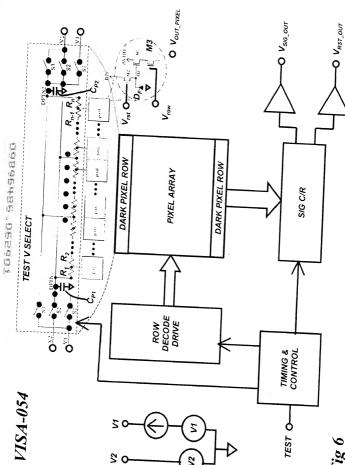


Fig 6

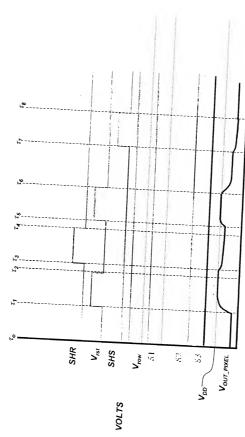


Fig 7a

TIME

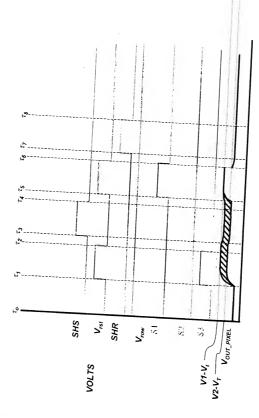


Fig 7b

TIME

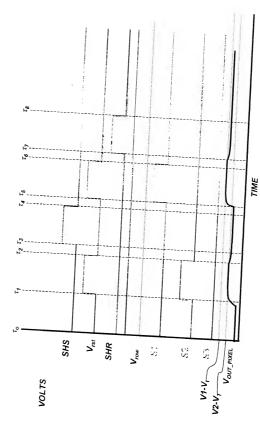


Fig 7c

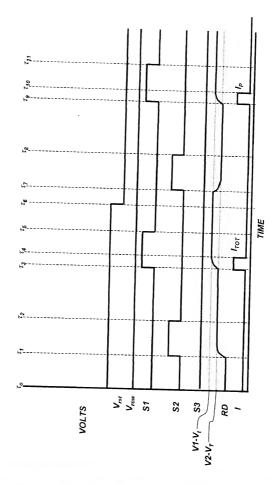
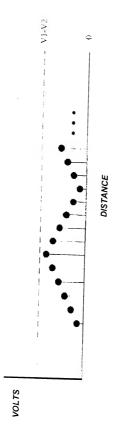


Fig 7d



Fig~8

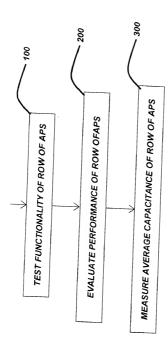
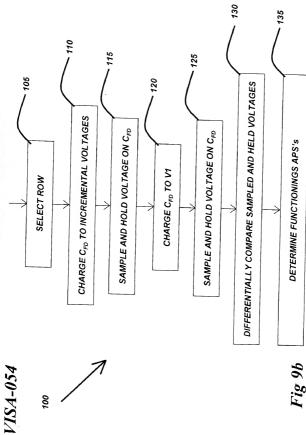


Fig 9a



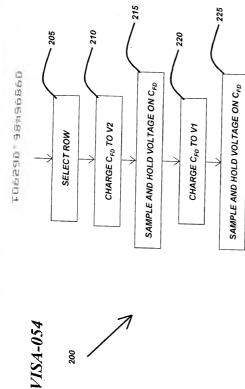


Fig 9c

DETERMINE PERFORMANCE APS'S

230

DIFFERENTIALLY COMPARE SAMPLED AND HELD VOLTAGES

- 235

VISA-054

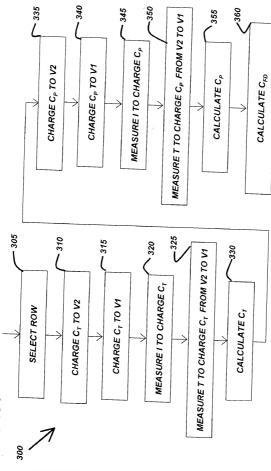


Fig 9d